

Substitute for Form 1449 A & B/PTO			<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Application Number	10/508,795
			Confirmation Number	3903
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			First Named Inventor	Tatsuo TSUJITA
			Art Unit	2819
			Examiner Name	Brian K. Young
			Attorney Docket Number	Q83757
Sheet	1	of		

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ³ (if known)		
BY		US 2001/052864	A1	12/20/2001	Shimizu Atsushi et al.
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BY		US 5,764,299	A	06/09/1998	Jennes et al
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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ³ (if known)		
BY		JP	03-046883	A	Shimadzu Corp.	Abstract
BY		EP	1 058 234	A	Matsushita Electric Industrial Co. Ltd.	
BY		JP	63 121385	A	Sanyo Electric Co. Ltd.	Abstract
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BY		EP	0 457 329	A	Nippon Telegraph and Telephone Corporation; Hosiden Corporation	
BY		EP	1 043 839	A	Texas Instruments Incorporated	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), date of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
BY		HUAWEN JIN ET AL; Time-interleaved A/D converter with channel randomization; CIRCUITS AND SYSTEMS, 1997. ISCAS '97, PROCEEDINGS OF 1997 IEEE INTERNATIONAL SYMPOSIUM ON HONG KONG 9-12 JUNE, 1997, NEW YORK, NY, USA, IEEE US vol. 1, 9 June, 1997 (1997-06-09), pg. 425-428.	
BY		PETRAGLIA A. ET AL.; Effects of quantization noise in parallel arrays of analog-to-digital converters; CIRCUITS AND SYSTEMS, ISCAS '94, 1994 IEEE INTERNATIONAL SYMPOSIUM ON LONDON, UK 30 May -2 June, 1994; New York, NY, USA, IEEE, US vol. 5, 30 May, 1994 (1994-05-30); pg. 337-340.	

Examiner Signature	BY	Date Considered	1/13/06
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